

FIG. 2

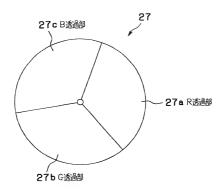


FIG. 3

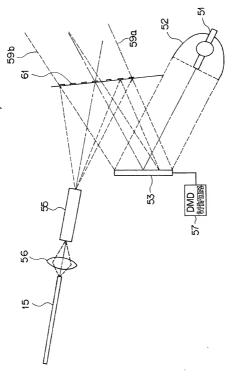
R露光	遮光	G露光	遮光	B露光	遮光	R露光]
1.3ms	4ms					時間	->

FIG. 4

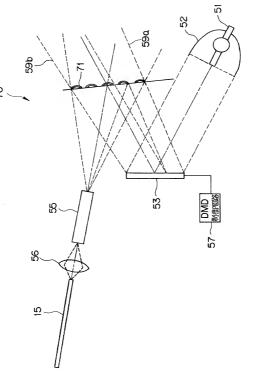
R露光	遮光	G露光	遮光	B露光	遮光	R露光	
10ms	7ms					時間	->

FIG. 6

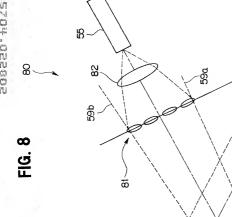
8







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DMD 制御回路

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FIG. 9

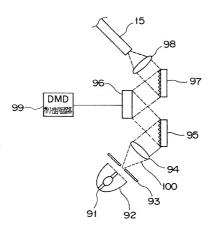


FIG. 10

